

MU at ARTEMIS and ITEA 2 Co-summit

Publicado el [2012/11/02](#) por [ruribeetxeberria](#)

[ARTEMIS and ITEA 2 Co-summit 2012, "Sharing a Vision for ICT Innovation"](#)

The fifth edition of the annual Co-summit has taken place in Paris, the 30th and 31st of October. About 650-700 participants from industry, academia, public authorities and press from all over Europe have participated in the Co-summit 2012. ARTEMIS puts "Intelligence on the spot" with an important component at this upcoming Co-summit 2012: the exhibition of the projects emanating from the ARTEMIS calls of 2008, 2009, 2010 and 2011! This year's theme for this event has been: Sharing a vision for ICT innovation.



ARTEMIS Industry Association is the association for R&D actors in Embedded Systems. The Industry Association represents an influential network of more than 200 members from all over Europe. The members of ARTEMIS Industry Association define the ARTEMIS Strategic Research Agenda (SRA) for Embedded Systems in Europe. The Industry Association is the

voice of its members in the ARTEMIS Joint Undertaking collaboration (<http://www.artemis-ju.eu/>). ARTEMIS Joint Undertaking is the Public Private Partnership with the European Commission, 23 ARTEMIS Member States and the ARTEMIS Industry Association that represent 200+ members. The ARTEMIS JU supports R & D Activities through open and competitive calls for proposals published on a yearly base, to attract the best European research ideas and capacities in the field of Embedded Computing systems

In a global world, Embedded Systems are a crucial key enabling technology for Europe's industrial and societal future which the ARTEMIS projects has been showcasing on the spot. For two days, CNIT in La Défense, Paris has been the 'beating heart' and global village for all actors in embedded systems.

Very interesting projects, all ranging from large-scale consortia to smaller but still cross-disciplinary projects, have been exhibited in the Co-summit, including four projects where Mondragon Unibertsitatea is participating:

- nShield (embedded Systems architecture for multi-Layer Dependable solutions): This project is the continuation of pShield project. It will provide a roadmap to address Security, Privacy and Dependability (SPD) by developing new technologies and consolidating those already explored in pShield. The state of the art in SPD of single technologies and solutions will be improved and integrated with an innovative, modular, composable, expandable and high-dependable architectural framework, concrete tools and common SPD metrics.
- pSafecer (pilot Safety Certification of Software-Intensive Systems with Reusable Components): pSafecer targets greater efficiency and reduced time-to-market by composable safety certification of safety-relevant embedded systems in the automotive and construction equipment, avionics and rail segments. pSafecer will also develop certification guidelines and a training example for other domains, thus considerably increasing its market impact. pSafecer brings together leading companies and SMEs across Europe along with selected universities and research institutes.
- nSafecer (Safety Certification of Software-Intensive Systems with Reusable Components): nSafecer is the continuation of pSafecer project. It focuses on composable safety certification of safety-relevant embedded systems through the development of efficient and industrial-strength methods and processes for the development and certification of these systems. European industry can achieve a leading position in the growing global market of safety-relevant embedded systems.
- Crafters (ConstRaint and Application driven Framework for Tailoring Embedded Real-time Systems): Crafters aims to significantly reduce total cost of ownership, time-to-market and the number of development assets by introducing a holistically designed ecosystem through a tightly integrated multi-vendor solution and tool chain that complements existing standards. Feature-limited releases of reference tools will be released and platforms will become available to support the evaluation and adoptions of the results.

Successfully completed ARTEMIS projects were exhibited in the Artemis Walk of Fame projects. Mondragon Unibertsitatea participated in two of the projects that were exhibited:

- pShield (pilot embedded Systems architecture for multi-Layer Dependable solutions):
The pilot version (hence: p.S.H.I.E.L.D) of the S.H.I.E.L.D project was intended to be a pioneer investigation to address Security, Privacy and Dependability (SPD) in the context of embedded systems as “built in” rather than as “add-on” functionalities. It proposed the first step towards SPD certification for future embedded systems.
- eDiana (Embedded Systems for Energy Efficient Buildings): eDiana has created a multi-faceted, multi-purpose framework for building sector to access, handle and optimise energy consumption in Cells (living/working units) and MacroCells (residential buildings) and so reduce energy demand as well as allow utility companies to more effectively manage energy load and allow customers to adjust consumption and to make real-time-based decisions.